IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of: HEA-SUK JUNG Application No.: Filed: For: **delay locked loop (dll) in semiconductor device**	Art Group: Examiner:

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure, enclosed is a copy of Information Disclosure Statement by Applicant (form PTO/SB/08), which is being submitted concurrently with the Utility Application. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s). Copies of the references cited on PTO/SB/08 are enclosed herewith.

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: 8/5/03

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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet	1	of	2

Complete if Known	
Application Number	
Filing Date	
First Named Inventor	Hea-Suk Jung
Art Unit	
Examiner Name	
Attorney Docket Number	51876P367

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Document Number Number - Kind Code[2] *(if known)*	Publication Date or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US-2003/0085744 A1	05-08-2003	Heo et al.	
		US-2003/0001637 A1	01-02-2003	Jung	
		US-6075395	06-13-2000	Saeki	
		US-6492852	12-10-2002	Fiscus	
		US-6552955 B1	04-22-2003	Miki	
		US-2003/0081491 A1	05-01-2003	Miki	
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Foreign Patent Document Country Code[3] - Number[4] - Kind Code[5] *(if known)*	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T[6]
		11-259167	09-24-1999	Internatl Business Mach Corp		
		11-214986	08-06-1999	Fujitsu Ltd		
		11-072540	03-16-1999	Fujitsu Ltd		
		2001-283589	10-12-2001	Fujitsu Ltd		
		2000-100163	04-07-2000	Fujitsu Ltd		

Examiner Signature		Date Considered	

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

[1]Applicant's unique citation designation number (optional). [2]See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. [3]Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). [4]For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. [5]Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. [6]Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08A (05-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 05/02/2003.

Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet	2	of	2

Complete if Known	
Application Number	
Filing Date	
First Named Inventor	Hea-Suk Jung
Art Unit	
Examiner Name	
Attorney Docket Number	51876P367

OTHER ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]

Examiner Signature		Date Considered	

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Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Information Disclosure Statement

New U.S. Patent Application for
DELAY LOCKED LOOP (DLL) IN SEMICONDUCTOR DEVICE
Our Ref. No.: P03H1037/US/wy

Reference No.:

(1) US Patent No. 6,552,955
(2) US Patent No. 6,492,852
(3) US Patent No. 6,075,395
(4) US Publication No. 2003/0085744 A1
(5) US Publication No. 2003/0081491 A1
(6) US Publication No. 2003/0001637 A1
(7) JP Laid-Open No. 2001-283589
(8) JP Laid-Open No. 2000-100163
(9) JP Laid-Open No. 11-259167
(10) JP Laid-Open No. 11-214986
(11) JP Laid-Open No. 11-72540